



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#12/C/NE)  
7/9/31  
Sunder

In re Application of:

**Mann, et al.**

Serial No.: 09/680,036

Filed: October 5, 2000

For: **Tapered Threshold Reset FET for  
CMOS Imagers**

Art Unit: 2811

Examiner: Kang, Donghee

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JUN 18 2003  
TECHNOLOGY CENTER 2800

**RESPONSE TO FINAL OFFICE ACTION**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir/Madam:

This is in response to the *Final Office Action* dated March 13, 2003 in the above-referenced patent application. Please enter and consider the following amendments and remarks.

06/17/2003 MDAYTE1 00000002 09630036